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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

TI-31089

Mahalingam Nandakumar, et al.

Examiner: Edgardo Ortiz

Serial No: 09/876,292

Art Unit: 2815

Filed: 06/07/01

For: Additional n-Type LDD/Pocket Implant for Improving Short-Channel NMOS ESD  
Robustness

**INFORMATION DISCLOSURE STATEMENT UNDER 37 CFR 1.97 & 1.98**

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

**MAILING CERTIFICATE UNDER 37 C.F.R. §1.8(A)**

I hereby certify that the above correspondence is being deposited with the U.S. Postal Service as First Class Mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

Karen Vertz  
Karen Vertz

1-8-04  
Date

Dear Sir:

In the matter of the above-identified application, Applicant is submitting herewith copies of documents listed in the attached form equivalent to Form PTO-1449 for the Examiner's consideration.

In accordance with 37 CFR 1.97 (d), this Information Disclosure statement is being submitted before payment of the issue fee and is accompanied by the fee of \$130.00 set forth in 37 CFR 1.17(i), and the following certification specified in 37 CFR 1.97(e).

On information and belief, I hereby certify that each item of information contained in this Information Disclosure Statement was cited in a communication from a foreign patent

01/14/2004 MBELETE1 00000125 200668 09876292

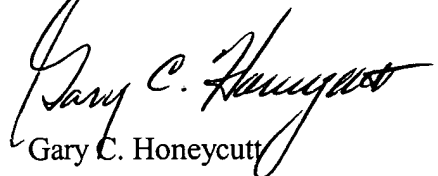
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office in a counterpart foreign application not more than three months prior to the filing of this statement.

Pursuant to 37 CFR 1.97(d), Applicant respectfully petitions that this Information Disclosure Statement be considered by the Examiner.

Please charge any shortage in the fees due in connection with the filing of this paper, includes extension of time fees, to the Deposit Account No. 20-0668 of Texas Instruments Incorporated, and please credit any excess fees to such Deposit Account. **This form is submitted in triplicate.**

Respectfully submitted,

A handwritten signature in black ink, appearing to read "Gary C. Honeycutt", written over the printed name.

Gary C. Honeycutt

Attorney for Applicants

Reg. No. 20,250

Texas Instruments Incorporated  
P.O. Box 655474, MS 3999  
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(972) 470-0130

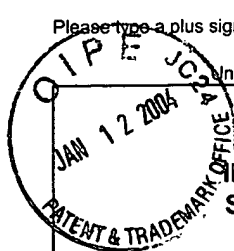
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Approved for use through 10/31/2002. OMB 0651-0031

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Substitute for Form 1449A/PTO

# **INFORMATION DISCLOSURE STATEMENT BY APPLICANT**

(use as many sheets as necessary)

Sheet	1	of	1	Application Number	09/876,292
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## **Complete If Known**

Filing Date	06/07/01
First Named Inventor	Mahalingam Nandakumar, et al.
Group Art Unit	2815
Examiner Name	Edgardo Ortiz
Attorney Docket No.	TI-31089

## **U.S. PATENT DOCUMENTS**

Exam. Initials*	Cite No. <sup>1</sup>	U.S. Patent Document		Name of Patentee or Applicant of Cited Doc.	Date of Pub. of Cited Doc. (mm-dd-yyyy)	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number	Kind Code <sup>2</sup> (if known)			
	AA	5,023,678		Kinzer	6/11/91	
	AB	5,371,394		Ma et al.	12/6/94	
	AC	6,174,778		Chen et al.	1/16/01	
	AD					
	AE					
	AF					
	AG					
	AH					
	AI					
	AJ					
	AK					
	AL					

## **FOREIGN PATENT DOCUMENTS**

Exam. Initials*	Cite No. <sup>1</sup>	Foreign Patent Document			Name of Patentee or Applicant of Cited Doc.	Date of Pub. of Cited Doc. (mm-dd-yyyy)	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T <sup>6</sup>
		Office <sup>3</sup>	Number <sup>4</sup>	Kind Code <sup>2</sup> (if known)				
	BA	FR	2 796 204 A1		Alieu Jerome et al.	1/12/01		
	BB							
	BC							
	BD							
	BE							
	BF							
	BG							

## **OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS**

Exam. Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
	CA	KUNIKIYO TATSUYA, "Semiconductor Device and Its Manufacture" Patent Abstracts of Japan, Vol. 1995, No. 02, March 31, 1995, Publication Number 06318698, Publication Date November 15, 1994	
	CB	MORII TOMOYUKI, "MOS Type Semiconductor Device and Manufacture Thereof" Patent Abstracts of Japan, Vol. 014, No. 346, July 26, 1990, Publication Number 02119269, Publication Date May 7, 1990	
	CC		
	CD		
	CE		
	CF		
	CG		

Examiner Signature		Date Considered	
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\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

**US and Foreign Patent Documents:** <sup>1</sup>Unique citation designation number. <sup>2</sup>See attached Kinds of U.S. Patent Documents. <sup>3</sup>Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). <sup>4</sup>For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. <sup>5</sup>Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. <sup>6</sup>Applicant is to place a check mark here if English language Translation is attached.

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